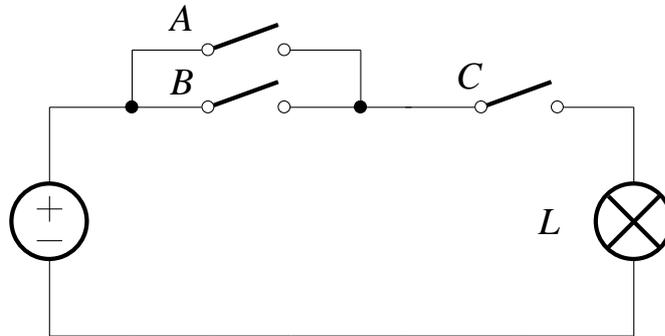


## 9 Digital Logic

1.

Express the following switching circuit in binary logic notation:



2.

Given the Boolean function:

$$F = xy + \bar{x} \bar{y} + \bar{y}z$$

- Implement with AND, OR and NOT gates.
- Implement with only OR and NOT gates.
- Implement with only AND and NOT gates.
- Implement with only NAND gates.